

an electroluminescence element electrically connected to the second thin film transistor,

wherein the first thin film transistor comprises at least two channel regions in an active layer, at least two gate electrodes corresponding to the channel regions, over the active layer with a gate insulating film interposed therebetween, and an impurity region interposed between the channel regions.

4

(Twice Amended) An electroluminescence display device comprising:

a substrate; and

a plurality of pixels over the substrate, each of the plurality of pixels comprising:

a first thin film transistor;

a second thin film transistor comprising a gate electrode electrically connected to the first thin film transistor; and

an electroluminescence element electrically connected to the second thin film transistor,

wherein the first thin film transistor comprises at least two channel regions in an active layer, at least two gate electrodes corresponding to the channel regions, over the active layer with a gate insulating film interposed therebetween, and an impurity region interposed between the channel regions, and

wherein a channel width of the second thin film transistor is greater than a channel width of the first thin film transistor.

17. (Amended) An electroluminescence display device comprising:

a substrate; and

a plurality of pixels over the substrate, each of the plurality of pixels

comprising:

a first thin film transistor;

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a second thin film transistor comprising a gate electrode electrically connected to the first thin film transistor; and

an electroluminescence element electrically connected to the second thin film transistor,

wherein the first thin film transistor comprises at least two gate electrodes over the substrate, at least two channel regions corresponding to the gate electrode, over the gate electrode with a gate insulating film interposed therebetween, and an impurity region interposed between the channel regions.

25. (Amended) An electroluminescence display device comprising:

a substrate; and

a plurality of pixels over the substrate, each of the plurality of pixels comprising:

a first thin film transistor;

a second thin film transistor comprising a gate electrode electrically connected to the first thin film transistor; and

an electroluminescence element electrically connected to the second thin film transistor,

wherein the first thin film transistor comprises at least two gate electrodes over the substrate, at least two channel regions corresponding to the gate electrode, over the gate electrode with a gate insulating film interposed therebetween, and an impurity region interposed between the channel regions, and

wherein a channel width of the second thin film transistor is greater than a channel width of the first thin film transistor.

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